Our Ref. No. 042390P17603 Express Mail No.: EV323393286US

## UNITED STATES PATENT APPLICATION

**FOR** 

# FLUXES FOR FLIP CHIP ASSEMBLY USING WATER SOLUBLE POLYMERS

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#### FLUXES FOR FLIP CHIP ASSEMBLY USING WATER SOLUBLE POLYMERS

## **BACKGROUND**

### FIELD OF THE INVENTION

[001] Embodiments of the invention relate to the field of semiconductor, and more specifically, to flip chip assembly.

### **DESCRIPTION OF RELATED ART**

[002] Low dielectric constant (low-k) materials are used in interlayer dielectrics (ILD) in semiconductor devices to reduce propagation delay and improve device performance. Use of low-k ILD has presented challenges to device packaging applications. Among various techniques, flip chip die mounting has been the preferred approach for a wide variety of integrated circuit (IC) packaging applications. The technology has created a number of problems. One particular problem in flip chip assembly is the stress induced during chip attachment, causing de-lamination, cracking, or other damages to the low-k ILD inside the die. The stress is mainly caused by coefficient thermal expansion (CTE) mismatch between the die and the substrate.

[003] Existing techniques are inadequate to solve this problem. High residue and high modulus flux can only provide marginal protection for low-k ILD and cannot provide full protection for future low-k ILD materials. In addition, any flux residue may cause downstream capillary underfill process problems and negatively impact device reliability.

## BRIEF DESCRIPTION OF THE DRAWINGS

- [004] The invention may best be understood by referring to the following description and accompanying drawings that are used to illustrate embodiments of the invention. In the drawings:
- [005] Figure 1 is a diagram illustrating a system in which one embodiment of the invention can be practiced.
- [006] Figure 2A is a diagram illustrating a flux dispensing according to one embodiment of the invention.
- [007] Figure 2B is a diagram illustrating chemical structures of polymers according to one embodiment of the invention
- [008] Figure 2C is a diagram illustrating a die placement according to one embodiment of the invention.
- [009] Figure 2D is a diagram illustrating a reflow according to one embodiment of the invention.
- [0010] Figure 2E is a diagram illustrating a de-fluxing according to one embodiment of the invention.
- [0011] Figure 2F is a diagram illustrating an under-fill dispensing according to one embodiment of the invention.
- [0012] Figure 3A is a diagram illustrating a reflow temperature profile for flux having monomer according to one embodiment of the invention.
- [0013] Figure 3B is a diagram illustrating a reflow temperature profile for flux having polymer according to one embodiment of the invention.
- [0014] Figure 4 is a flowchart illustrating a process for flip-chip assembly according to one embodiment of the invention.
- [0015] Figure 5 is a flowchart illustrating a process for reflow according to one embodiment of the invention.

#### DESCRIPTION

[0016] An embodiment of the present invention is a technique to utilize water soluble polymer-containing flux to protect low-k ILD of flip chip devices during assembly. A flux which includes at least a solvent and one of a water soluble monomer or a water soluble polymer or a traditional flux agent is applied on substrate After a die is placed on the flux on the substrate, the die is reflowed in a reflow oven at a reflow temperature which is higher than the melting point of the polymer to form solder joints, and during cooling, the molten polymer solidifies to redistribute stress caused by coefficient thermal expansion (CTE) mismatch between the substrate and the die.

[0017] In the following description, numerous specific details are set forth. However, it is understood that embodiments of the invention may be practiced without these specific details. In other instances, well-known circuits, structures, and techniques have not been shown in order not to obscure the understanding of this description.

[0018] One embodiment of the invention may be described as a process which is usually depicted as a flowchart, a flow diagram, a structure diagram, or a block diagram. Although a flowchart may describe the operations as a sequential process, many of the operations can be performed in parallel or concurrently. In addition, the order of the operations may be re-arranged. A process is terminated when its operations are completed. A process may correspond to a method, a procedure, a method of manufacturing or fabrication, etc.

[0019] Figure 1 is a diagram illustrating a system 100 in which one embodiment of the invention can be practiced. The system 100 includes a flux application 110, a die placement 120, a reflow 130, a de-fluxing 140, and an under-fill dispensing 150.

[0020] The flux application 110 applies or dispenses flux on a substrate which has prefabricated solder bumps. The flux contains at least a water soluble monomer or polymer. The flux application 110 may be a stencil printing fluxing, a spray fluxing, or any other suitable methods. The die placement 120 then picks, aligns and places a die onto the substrate. The die is an integrated circuit (IC) device or a chip. The die may be picked from a feeder, tape and reel and is positioned to align with the solder bumps on the substrate. The reflow 120 forms the solder joints from the solder bumps and the flux in a reflow oven heated at a reflow temperature. The de-fluxing 140 removes the flux residues and cleans the solder joints. The under-fill dispensing 150 dispenses

under-fill material around the die. The under-fill is allowed to flow and fill the gap between the flip-chip and the substrate.

[0021] The flux used in the flux application 110 includes a solvent and at least one of a water soluble monomer or polymer and a traditional flux agent if the monomer or polymer does not have fluxing capability. The use of the water soluble monomer or polymer in the flux offers a number of advantages. First, it provides a high mechanical strength, or modulus, and high level residue to offset the CTE mismatch between the die and the substrate. This will lead to better protection for low-k ILD. Second, the cleaning process is facilitated. The flux residue can be completely removed by hot deionized (DI) water leading to significant improvement in device reliability. Third, it allows the use of capillary under-fill without void and flux residue-related problems. Fourth, it is compatible with conventional assembly process from flux printing to chip attachment, to de-flux or cleaning, and then to capillary under-fill with no or minimum impact.

[0022] Figure 2A is a diagram illustrating a flux application 110 according to one embodiment of the invention. The flux application 110 may be performed in one of two processes, process 201 and process 202.

[0023] In process 201, the flux 215 is printed on the pre-fabricated solder bumps 215 on a substrate 210 utilizing a stencil 235 and a squeegee 230. In process 202, the flux 215 is dispensed onto the pre-fabricated solder bumps on a substrate 210 utilizing a dispenser 225.

[0024] In both processes, the flux 215 includes a solvent, a water soluble polymer or monomer, a traditional flux agent such as organic acid if the polymer or monomer does not have fluxing capability, and other additives. The solvent may be organic solvent, water, or mixtures. The additives include surfactant, wetting agent, and viscosity modifier. When a monomer is used, it will polymerize to form a polymer during the reflow 120. The polymer or the polymerized monomer has a melting point lower than the reflow temperature so that it will not affect the solder joint formation during the reflow process. The water soluble polymer may be any one of a polyacrylic acid, a polyacrylamide, a polyvinyl alcohol, a modified starch, and a modified cellulose.

[0025] Figure 2B is a diagram illustrating chemical structures of polymers according to one embodiment of the invention

[0026] The polyacrylic acid may be used as a flux agent while the polyacrylamide and polyvinyl alcohol may need to be combined with a traditional fluxing agent such as Rosin to be used as a flux agent. The polymers with a melting temperature below 183°C can be used for Sn/Pb eutectic solder joint while those with a melting point below 200°C can be used for lead-free solder joint like Sn/Ag, Sn/Cu, or Sn/Ag/Cu. For example, polyacrylamide and polyvinyl alcohol have melting points of 84°C and 200°C, respectively. Therefore, polyacrylamide can be used for flip chip assembly with eutectic SnPb solder, and polyvinyl alcohol can be used for flip chip assembly with lead-free SnAg solder.

[0027] The use of the above polymers offer a number of advantages. First, there is a high polymer residue level left after the chip attachment process. Second, there is a high adhesion of the polymeric residue to both the die and the substrate surfaces. Third, there is a high modulus (greater than 1 GPa) of the polymeric residue to provide the mechanical strength and redistribute the internal stress generated due to the CTE mismatch between the die and substrate after assembly and protect the weak low-k ILD of the die. Fourth, the polymeric residue can be dissolved in hot de-ionized (DI) water and removed by hot DI water cleaning. This complete removal of the residue increases the device reliability significantly.

[0028] Figure 2C is a diagram illustrating a die placement 120 according to one embodiment of the invention.

[0029] A die holder 232 holds a die 235 and places it onto the pre-fabricated solder bumps 220 on the substrate 210. The die 235 is an integrated circuit (IC) or a chip. It may be picked from a feeder mechanism, such as waffle pack feeders, tape and reel, surf tape or a direct wafer feeder. The bumps 236 of the die 235 are aligned with the solder bumps 220 on the substrate 210 and the die 235 is then placed onto the substrate 210.

[0030] Figure 2D is a diagram illustrating a reflow process 130 according to one embodiment of the invention.

[0031] The assembly including the die 235 and the substrates 210 together with the flux 215 is placed in a reflow oven 240. The reflow oven 240 has a temperature controller 245 to control the reflow temperature. During reflow, the reflow temperature is increased to a maximum temperature and then decreased to cool down.

At the increasing temperature, the metal oxide on the substrate and die bumps is removed by the polymer acid or additive acid. The solder joints are then formed while the polymer is in the melting state, e.g., in liquid form. During cooling down, the polymer is hardened or solidified. The stress in the solder bumps caused by CTE mismatch is re-distributed by the solidified polymer. The low-k ILD is then protected from damage by the high modulus polymer residue.

[0032] Figure 2E is a diagram illustrating a de-fluxing 140 according to one embodiment of the invention.

[0033] After the reflow 130, there is flux residue 260 left between the die 235 and the substrate 210. The flux residue consists of primarily the solid water soluble polymer, residue for the additive acid, and other residues from the additives. A de-flux machine 250 sprays a de-flux material 255 into the die and substrate assembly to remove the flux residue 260. The de-flux material 255 may be hot DI water. The polymeric residue 260 is dissolved easily in hot DI water and is completely removed from the die assembly.

[0034] Figure 2F is a diagram illustrating an under-fill dispensing 150 according to one embodiment of the invention.

[0035] An under-fill dispenser 270 dispenses an under-fill 275 around the die 235. The under-fill 275 fills the gap between the die 235 and the substrate 210 through capillary action.

[0036] Figure 3A is a diagram illustrating a reflow temperature profile 300 for flux having monomer according to one embodiment of the invention. The reflow temperature profile 300 includes regions 310, 320, 330, and 340.

[0037] The regions 310 and 320 correspond to increasing temperature. During the region 310, the monomer in the flux reacts to form a solid polymer and the flux mixture removes the metal oxide from the solder bumps. During the region 320, the solid polymer melts into a polymer liquid.

[0038] The region 330 corresponds to approximately constant temperature. During this region, the solder bumps melt and form solder joints. The region 340 corresponds to decreasing temperature, or cooling down. During the region 340, the solder joints solidify. The molten polymer flux also solidifies to re-distribute the stress generated by

the CTE mismatch between the die and the substrate. This will provide protection to low-k ILD.

[0039] Figure 3B is a diagram illustrating a reflow temperature profile 345 for flux having polymer according to one embodiment of the invention. The reflow temperature profile 345 includes regions 350, 360, and 370.

[0040] The region 350 correspond to increasing temperature. During the region 350, the flux mixture removes the metal oxide from the solder bumps, and the solid polymer melts into a polymer liquid.

[0041] The region 360 corresponds to approximately constant temperature. During this region, the solder bumps melt and form solder joints. The region 370 corresponds to decreasing temperature, or cooling down. During the region 370, the solder joints solidify. The molten polymer flux also solidifies to re-distribute the stress generated by the CTE mismatch between the die and the substrate. This will provide protection to low-k ILD.

[0042] Figure 4 is a flowchart illustrating a process 400 for flip-chip assembly according to one embodiment of the invention.

[0043] Upon START, the process 400 applies a flux on a substrate which may have pre-fabricated solder bumps (Block 410). The flux includes a solvent, a water soluble monomer or polymer, or a traditional flux agent.

[0044] Next, the process 400 places a die on the substrate (Block 420). The die is aligned with the substrate. Then, the process 400 reflows the die assembly in a reflow over at a reflow temperature to form electrical interconnections between the die and substrate while the polymer is in molten state, and during cooling down, the polymer solidifies, re-distribute the stress caused by the CTE mismatch between the die and the substrate (Block 430). The reflow temperature is higher than the melting point of the polymer. The process 400 is then terminated.

[0045] Figure 5 is a flowchart illustrating a reflowing process 430 according to one embodiment of the invention.

[0046] Upon START, several processes may happen simultaneously (block 510). These processes include monomers reacting to form polymer, flux removed of metal

oxide, solvent vaporization, and a polymer melting. Next, the process 430 melts the solder bumps (Block 535). Next, the process 430 forms solder joints from the solder bumps (Block 540).

[0047] Then, the process 430 solidifies the solder joints at decreasing reflow temperature (Block 545), e.g., during cooling. Next, the process 550 solidifies the polymer liquid to redistribute the stress (Block 550) and is then terminated.

[0048] While the invention has been described in terms of several embodiments, those of ordinary skill in the art will recognize that the invention is not limited to the embodiments described, but can be practiced with modification and alteration within the spirit and scope of the appended claims. The description is thus to be regarded as illustrative instead of limiting.